

From Box Algebra to Interval Temporal Logic

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Abstract. In this paper, we further develop a recently introduced semantic link between temporal logics and Petri nets. We focus on two specific formalisms, Interval Temporal Logic (ITL) and Box Algebra (BA), which are closely related by their compositional approach to constructing system descriptions. The overall goal of our investigation is to translate Petri nets into behaviourally equivalent logical formulas. As a result, the analysis of system properties can be carried out using either of the two formalisms, exploiting their respective strengths and powerful tool support.

The contribution of this paper is twofold. First, we extend the existing translation from BA to ITL, by removing restrictions concerning the way control flow of concurrent system is modelled, and by allowing a fully general synchronisation operator. Second, we strengthen the notion of equivalence between a Petri net and the corresponding logical formula by proving such an equivalence at the level of transition-based executions of Petri nets rather than just by looking at their labels. We also show that the complexity of the proposed translation compares favourably with the complexity of the translation from BA expressions to Petri nets.

Keywords: Interval Temporal Logic, Petri nets, box algebra, composition, semantics, general synchronisation, step sequence, equivalence

1. Introduction

Temporal logics [1, 2, 3, 4] and Petri nets [5, 6] are generally regarded as fundamentally different approaches to the specification and analysis of concurrent systems. The former allow one to specify both the system designs and correctness requirements within a single logical framework, and the verification of correctness can be done by checking the satisfaction of formulas, or model checking. In contrast, Petri nets are an automata-inspired model with semantics based on actions and local states which allows one to capture causal relationships in systems' behaviour. As a result, verification of behavioural properties can be carried out using invariant techniques [7], based on the graph structure of nets, or model checking techniques, based on partial order semantics [8, 9].

To establish a semantic link between logics and Petri nets, we focused in [10] on two specific formalisms, *Interval Temporal Logic* (ITL) [11, 12] and *Box Algebra* (BA) [13], which are closely related by their compositional approaches to constructing system descriptions. In particular, in both ITL and BA the control flow of a system is specified by means of commonly used programming operators, such as sequence, choice, parallelism, and iteration. The synchronisation between concurrently executed subsystems is, however, achieved in different ways and therefore needs to be suitably handled.

Box Algebra [13], and its precursor Petri Box Calculus [14], provided a generic process-algebraic syntax together with a compositional translation to a class of Petri nets called boxes. This generic algebra has several concrete incarnations, including CCS [15] and TCSP [16]. BA can also readily yield a compositional semantics for an imperative concurrent programming language [17]. It has also been extended to handle, e.g., stochastic aspects of concurrent systems [18].

In contrast to point-based temporal logics (e.g., [2, 3]), where computations are described in terms of successive system states, interval temporal logics aim at capturing the evolution of a system referring to its behaviour over time intervals. As pointed out in [19], the concept of time intervals is both naturally appealing and intuitive, and has been of significant interest to several disciplines, including Philosophy, Linguistics, Artificial Intelligence, and Computer Science. For example, interval temporal logics allow one to easily capture concepts relating to action duration or shared-variable concurrency (e.g., [20]). The preface [4] of a journal special issue on interval temporal logics identified, in particular, two categories of temporal logics formalisms for intervals: (i) modal formalisms such as HS-logics [21] and CDT [22] with interval-based variables and capable of expressing Allen's relations [23] (recent developments concerning model checking HS-logics include [24, 25, 26, 27, 28]); and (ii) Interval Temporal Logic (ITL) of [29, 30, 12] together with its variations and adaptations which use state-based variables and the sequential composition 'chop' operator to a significant extent. In this paper we focus on ITL which has influenced several research groups and projects, as well as IEEE standard 1647 ([31]). It is not unusual for researchers to develop their own variants of ITL, often with quite different names. The Duration Calculus [32], a prominent real-time variant of ITL, is the best example. A recent instance of this is Multi-Lane Spatial Logic [33], which is intended for modeling road traffic (e.g, for computer-based assistance of car drivers). ITL lends itself to execution, and can support abstract specifications and concrete implementations in the same notation, with refinement mappings between. Moreover, timings of executions can be easily derived by considering interval lengths.

1.1. About this paper

In [10], we proved the correctness of a translation from a submodel of BA to semantically equivalent ITL formulas. The submodel we considered disallowed the nesting of the parallel composition operator. Moreover, synchronisation was binary. In the paper [34], we demonstrated how to extend the submodel of [10] to handle data variables.

In this paper, which is a full and extended version of a conference publication [35], we provide a syntax-driven translation for the core BA [13] syntax comprising parallel composition, sequence, choice, synchronisation, and iteration but without considering data variables (incorporating these using the technique introduced in [34] would be straightforward but, at the same time, would obscure the key constructs introduced in the present paper). Crucially, we relax the syntactical constraints preventing the use of the parallel composition outside the topmost level of process expressions. Moreover, we consider a fully general synchronisation operator. Finally, we strengthen the notion of equivalence between a BA net and the corresponding ITL formula by proving such an equivalence at the level of transition-based executions of Petri nets, rather than just by taking their labels. It is worth noting that for the purposes of this paper, i.e., the translation of the basic BA, we only need to use propositional ITL (PITL).

To formalise a semantic link between BA and ITL, for every logical formula we introduce a step sequence semantics which records variables changing their values at each computational step. This allows us to compare its behaviour with the step sequences of the corresponding Petri net, and to conclude their full equivalence in the main result (Theorem 4.3). In essence, the latter states that any property which can be captured within the step sequence model can be analysed using either of the two equivalent representations, i.e., a Petri net or a logical formula.

1.2. Paper organisation

The paper is organised as follows. In the next section, we recall the basic notions of Box Algebra. We start with the definition of box expressions, which are then used to compositionally construct box nets. We also recall a number of results demonstrating how the execution semantics of a composite box can be derived from the execution semantics of its components. In Section 3, we similarly recall the relevant fragment of Interval Temporal Logic, and present some basic semantic properties of ITL formulas. Section 4 is central to the whole paper as it contains a formal translation from box expressions to ITL formulas as well as the proof of a semantic equivalence between box expressions and the corresponding formulas. The following section presents examples of this translation, and Section 6 briefly discusses future work.

1.3. An introductory example

In order to outline the main ideas behind the proposed solution, we will now consider a small fragment of an application involving two concurrent processes, a server and a client, following a simple interactive protocol where: (i) the client process sends a query to a database server, awaits the answer, and then carries out a local update; and (ii) the server process non-deterministically chooses between

handling a client request or performing its own local update:

Client	Server
\vdots	\vdots
$(\text{send.req} ; \text{receive.ans}) ; \text{local.upd}$	if
\vdots	case 1 : $\text{receive.req} ; \text{send.ans}$
	case 2 : local.upd
	fi
	\vdots

From the above fragment of pseudo-code we can extract in a straightforward way the following Box Algebra representation, where $;$ and \square respectively denote sequential composition and non-deterministic choice:

$$Client = (s.req ; r.ans) ; l.upd \quad Server = (r.req ; s.ans) \square l.upd.$$

The box semantics of the above expressions is obtained compositionally, by first associating basic boxes (single-transition Petri nets) with atomic box expressions, and then by applying Box Algebra compositions rules. In general, Petri nets are directed graphs with two kind of nodes: *places (local states)* represented by circles, and *transitions (actions)* represented by squares or rectangles. The arcs connect nodes of different types and indicate direct relationships between places and transitions. In the box nets, additionally, circles labelled with e are entry places, circles labelled with i are internal places, and circles labelled with x are exit places, reflecting their intended role in net composition.

The successive construction stages of the box semantics for *Server*

$$N_{Server} = N_{(r.req ; s.ans) \square l.upd} = (N_{r.req} ; N_{s.ans}) \square N_{l.upd}$$

are shown in Figure 1. First, two basic boxes $N_{r.req}$ and $N_{s.ans}$ (the two Petri nets in Figure 1(a)) are composed sequentially by gluing the exit (or final) place of $N_{r.req}$ with the entry (or initial) place of $N_{s.ans}$. The resulting box $N_{r.req ; s.ans}$ (the Petri net in Figure 1(b)) is then composed with the basic box $N_{l.upd}$ by applying the non-deterministic choice operation, which glues together their entry places as well as their exit places (the final Petri net is shown in Figure 1(c)).

The net N_{Server} we have just constructed provides the graph-theoretic (static) representation of the *Server* process. To obtain its dynamic (behavioural) representation, we need in addition to specify the initial marking (or state) of *Server*, from which one can explore all possible execution paths by following the standard transition firing rules. In the case of a box net, this is simply done by inserting a single token in each of its entry places (in this case, just one place). The initial marking can then be used to investigate the dynamic behaviour of *Server*, which is basically composed of two execution paths, both ending at a final marking in which the only exit place contains a token. One such path executes $r.req$ followed by $s.ans$, and the other executes a single transition $l.upd$.

The states of the *Server* process are all the markings reachable from the initial one, and possible executions are represented by sequences of executed transitions (or sets of transitions, called steps).

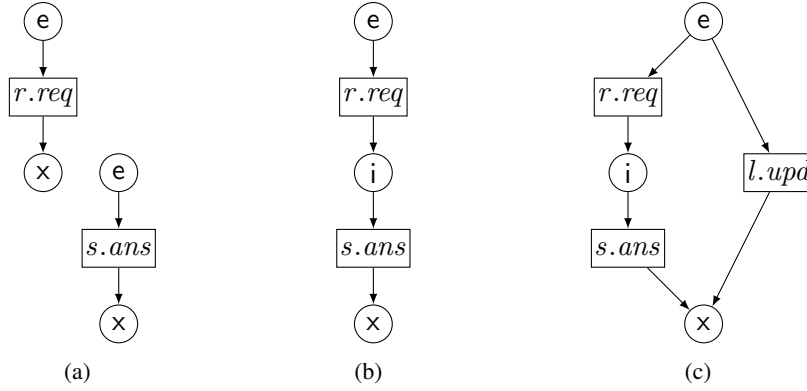


Figure 1. Compositional derivation of the box $N_{Server} = N_{(r.req ; s.ans)} \sqcap l.upd$: (a) basic boxes representing $r.req$ and $s.ans$; (b) sequential composition representing $r.req ; s.ans$; and (c) non-deterministic choice representing $Server$. Note that the entry and exit places are respectively labelled by e and x ; moreover, i identifies the internal places. All transitions are labelled by the corresponding atomic expressions occurring in the $Server$ process.

In the ITL context, however, a state is a mapping which assigns values to a set of (Boolean) variables, and possible executions are represented by sequences of such assignments, called intervals. Therefore, we need a method to bridge the gap between these two different ways of representing the states and executions of concurrent systems. The solution we adopt and develop in this paper is to associate with each transition t of N_{Server} a distinct Boolean variable, also denoted by t , and then to represent each firing of transition t by flipping the value of variable t ; otherwise, the value of variable t is kept unchanged. With this idea in mind, the box expression $Server$ can be captured by the following ITL formula:

$$\text{itl}(Server) = (\text{fs}(r.req \mid s.ans, l.upd) ; \text{fs}(s.ans \mid r.req, l.upd)) \vee \text{fs}(l.upd \mid r.req, s.ans).$$

where $;$ is the ITL “chop” operator¹ (sequential composition) and a formula of the form $\text{fs}(v \mid w, u)$ means that the values of the two variables w and u are kept unchanged while the value of v can be flipped once within the interval over which the formula is evaluated (if v is never flipped, the interval must be infinite). The execution path of N_{Server} in which transition $r.req$ is followed by $s.ans$ can then be matched by a corresponding interval satisfying $\text{itl}(Server)$ in which the variables $r.req$ and $s.ans$ flip their values, as shown below:

<i>executed transitions</i>	<i>r.req</i>	<i>s.ans</i>
<i>r.req</i>	0	1
<i>s.ans</i>	0	0
<i>l.upd</i>	0	0

¹Subsequently adopted, e.g., by the Duration Calculus [32].

Having seen how $Server = (r.req ; s.ans) \sqcap l.upd$ can be represented by an ITL formula, one might attempt to do exactly the same for a concurrent composition of both processes:

$$\begin{aligned} & ((fs(s.req \mid \dots) ; fs(r.ans \mid \dots)) \quad ; \quad fs(l.upd \mid \dots)) \\ & \wedge \\ & ((fs(r.req \mid \dots) ; fs(s.ans \mid \dots)) \quad \vee \quad fs(l.upd \mid \dots)) \end{aligned}$$

Such a naive translation exhibits two serious problems. The first is that it does not provide any means to synchronise the communication actions between the two processes. We address this issue by identifying some of the variables in different processes, such as $s.req$ and $r.req$. The other problem is quite an opposite one, namely, the formula specifies that two different local updates must happen simultaneously as they are represented by the same ITL Boolean variable $l.upd$. We address this problem by introducing two different variables which are allowed to flip their values independently of each other. Before illustrating how both solutions work, we will complete the presentation of the net model for the example system.

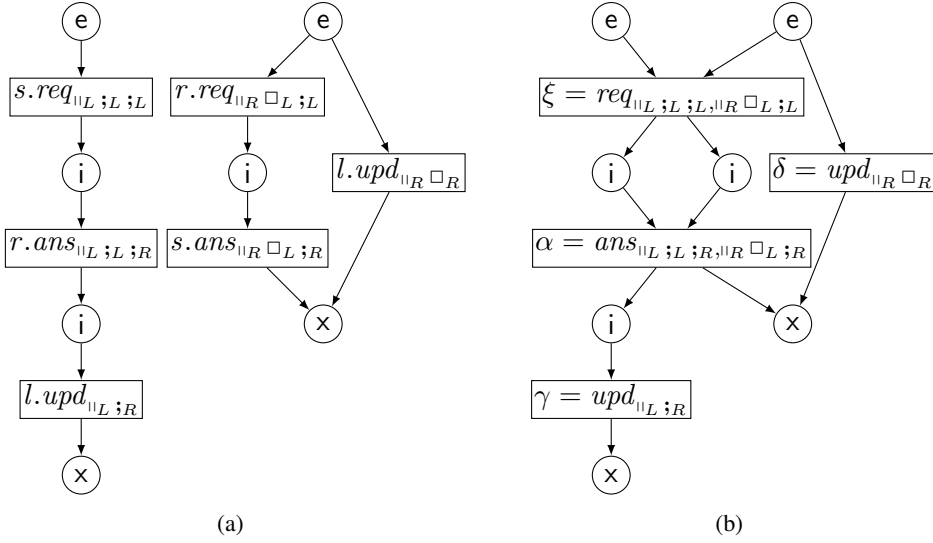
The whole synchronised system comprising a client and a server is captured by the following box expression, where \parallel is the parallel composition operator, and sco (for scoping) is an operator specifying all interprocess synchronisations [13]. For example, $s.req, r.req \mapsto req$ means that the simultaneously executed actions $s.req$ and $r.req$ can be synchronised, and replaced by a single new action req , while $l.upd \mapsto upd$ indicates that each action $l.upd$ is simply maintained, after being renamed. Here is the resulting system expressed in Box Algebra, where the upper line represents server and the lower receiver:

$$System = \left(\begin{array}{c} (s.req ; r.ans) ; l.upd \\ \parallel \\ (r.req ; s.ans) \sqcap l.upd \end{array} \right) sco \left\{ \begin{array}{l} s.req, r.req \mapsto req \\ s.ans, r.ans \mapsto ans \\ l.upd \mapsto upd \end{array} \right\}$$

The box N_{System} generated from the above expression is depicted in Figure 2. *A crucial point is that during its generation each action (and then the corresponding transition) is made unique by annotating it with the path(s) reflecting its position in the parse tree of the System expression.* (Note that the construction stages shown in Figure 1 would also be amended to include appropriate annotations.) For example, $r.req_{\parallel R \sqcap L ; L}$ means that $r.req$ is reachable in the parse tree through the following path: from the root, labelled with \parallel , go to its right child (R) corresponding to the second operand, labelled with \sqcap , and from there visit its left child (L) corresponding to the first operand, labelled with $;$ and, in turn, from here visit the left child (L) corresponding to the first operand, which is the node labelled with $r.req$.

Thanks to annotating the individual actions with unique syntax paths, one can avoid unwanted synchronisations of independent actions, and then obtain an ITL translation of the whole synchronised system expression:

$$\begin{aligned} itl(System) &= ((fs(\xi \mid \alpha, \gamma) ; fs(\alpha \mid \xi, \gamma)) \quad ; \quad fs(\gamma \mid \xi, \alpha)) \\ &\wedge \\ &((fs(\xi \mid \alpha, \delta) ; fs(\alpha \mid \xi, \delta)) \quad \vee \quad fs(\delta \mid \xi, \alpha)) \end{aligned}$$

Figure 2. Boxes of $Client \parallel Server$ (a), and $System$ (b).

where $\xi = req_{||L;L;L;||R;L;L}$, $\alpha = ans_{||L;L;R;||R;L;R}$, $\gamma = upd_{||L;R}$, and $\delta = upd_{||R;R}$. In this paper, we show that the executions of the box shown in Figure 2 are consistent with the intervals satisfying the ITL formula $itl(System)$ as, for example, in the following execution scenario:

executed transitions	ξ	α	γ	
ξ	0	1	1	1
α	0	0	1	1
γ	0	0	0	1
δ	0	0	0	0

1.4. Notation

The concatenation operator for sequences will be denoted by “.”. For a finite sequence δ and a set of sequences Δ , $\delta \cdot \Delta = \{\delta \cdot \delta' \mid \delta' \in \Delta\}$, and for a set of finite sequences Δ and a set of sequences Δ' , $\Delta \cdot \Delta' = \{\delta \cdot \delta' \mid \delta \in \Delta \wedge \delta' \in \Delta'\}$. The k -th element of a sequence δ will be denoted by $\delta_{(k)}$ and its length by $|\delta|$ ($|\delta| = \omega$ if δ is infinite). We will use the following notations involving sequences of empty sets: $\emptyset^* = \{\epsilon, \emptyset, \emptyset\emptyset, \dots\}$ is the infinite set of all finite sequences of empty sets, and $\emptyset^\omega = \{\emptyset\emptyset \dots\}$ is the set containing a single infinite sequence of empty sets.

2. Box Algebra

Let \mathcal{A} be a finite set of atomic actions. A synchronisation relation ρ is a finite set of tuples of actions (a_1, \dots, a_n, a) with $n \geq 1$. Intuitively, a_1, \dots, a_n represent n concurrent actions which can be synchronised to yield a single composite action with the label a . To reflect this intuition, we will often denote (a_1, \dots, a_n, a) by $a_1 \dots a_n \mapsto a$.

The syntax given below defines two kinds of box expressions, namely, non-synchronised expressions E capturing the control flow in a concurrent system, and synchronised expressions F (below a is an action and ρ a synchronisation relation):

$$\begin{aligned} E &::= \text{stop} \mid a \mid E;E \mid E \sqcap E \mid E \parallel E \mid \llbracket E \circledast E \circledast E \rrbracket \\ F &::= E \text{ sco } \rho \end{aligned}$$

The intuition behind the above syntax is that: (i) stop denotes a blocked process; (ii) a denotes a process which can execute an action $a \in \mathcal{A}$ and terminate; (iii) $E;E'$ denotes sequential composition; (iv) $E \sqcap E'$ denotes non-deterministic choice composition; (v) $E \parallel E'$ denotes parallel composition; (vi) $\llbracket E \circledast E' \circledast E'' \rrbracket$ denotes a loop with an initial part E , iterated part E' , and terminal part E'' ; and (vii) $E \text{ sco } \rho$ denotes scoping, which first creates all the synchronisations specified by ρ and then deletes all the actions of E .

2.1. Box nets

The semantics of box expressions is given through a mapping into Petri nets called boxes.

A *box* is a tuple $N = (P, T, F, \ell)$, where: (i) P and T are disjoint finite sets of respectively *places* (representing local states) and *transitions* (representing actions); (ii) $F \subseteq (P \times T) \cup (T \times P)$ is a *flow relation*; and (iii) ℓ is a *labelling function* for places and transitions such that $\ell(p) \in \{e, i, x\}$ associates an entry, internal, or exit status with every place p , and an atomic action $\ell(t) \in \mathcal{A}$, with every transition t .

The sets of entry, internal and exit places of N are given respectively by $N^e = \ell^{-1}(e)$, $N^i = \ell^{-1}(i)$, and $N^x = \ell^{-1}(x)$. Moreover, we set $N^{ei} = N^e \cup N^i$ and $N^{ix} = N^i \cup N^x$, and will use N^P , N^T , and N^F to respectively denote the places, transitions, and the flow relation (arcs) of N . We also adopt the standard rules about representing nets as directed graphs.

2.2. Semantics of box nets

The global states of a box N are called *markings*, each marking being a mapping M assigning a non-negative integer to every place in N^P . The default initial (or entry) and final (or exit) markings of N , denoted respectively by M_N^e and M_N^x , are defined, for every $p \in N^P$, as follows:

$$M_N^e(p) = \begin{cases} 1 & \text{if } p \in N^e \\ 0 & \text{otherwise} \end{cases} \quad \text{and} \quad M_N^x(p) = \begin{cases} 1 & \text{if } p \in N^x \\ 0 & \text{otherwise} \end{cases}$$

The change of a marking of N results from a simultaneous execution of a (possibly empty) set of transitions, called a *step*. Formally, a step of N is any set of transitions $U \subseteq N^T$. It is enabled at a

marking M if, for every place $p \in N^P$:

$$M(p) \geq |\{t \in U \mid (p, t) \in F\}|.$$

We denote this by $M[U]$. An enabled step U can be executed leading to a marking M' given, for every place $p \in N^P$, by:

$$M'(p) = M(p) - |\{t \in U \mid (p, t) \in F\}| + |\{t \in U \mid (t, p) \in F\}|.$$

We denote this by $M[U]M'$.

The semantics of N is given through its infinite step sequences starting from the default initial marking M_N^e . In addition, we single out a set of finite step sequences which lead from the default initial marking M_N^e to the default final marking M_N^x . Intuitively, each such step sequence will be interpreted as a successfully terminating execution of N .

A *step sequence* of N is an infinite sequence $\theta = U_1 U_2 \dots$ of steps such that there exist markings M_1, M_2, \dots of N satisfying

$$M_N^e[U_1]M_1 \ M_1[U_2]M_2 \ M_2[U_3]M_3 \ \dots$$

Moreover, we define a *terminating step sequence* of N as a finite sequence $\theta = U_1 \dots U_m$ ($m \geq 0$) of steps such that there exist markings M_1, \dots, M_{m-1} of N satisfying

$$M_N^e[U_1]M_1 \ M_1[U_2]M_2 \ M_2[U_3]M_3 \ \dots \ M_{m-1}[U_m]M_N^x.$$

The sets of (infinite) step sequences and terminating step sequences of N are respectively denoted by $\text{infsts}(N)$ and $\text{finsts}(N)$.²

2.3. Composite boxes

The above definition of a box net is too general for our purposes, as we are interested in nets derived compositionally from box expressions. The labelling of places will provide the necessary device for composing boxes along the entry and exit interfaces, i.e., the sets of entry places N^e and exit places N^x .

The nets we are going to construct will have a very specific form of places and transitions, making it easier to establish connections between boxes and ITL formulas. Intuitively, we will use the syntax of a non-synchronised box expression E to construct concrete places and transitions of the corresponding box N by embedding paths from the root of the parse tree of E in the definitions of places and transitions. In what follows, finite sequences in the set

$$\Pi = \{ ;_L, ;_R, \square_L, \square_R, \parallel_L, \parallel_R, \otimes_L, \otimes_M, \otimes_R \}^*$$

will be called *syntax paths*. Note that symbols appearing in syntax paths correspond to the arguments of operators used in box expressions (with ‘ L ’ indicating the left operand, \otimes_M the middle operand,

²Note that a deadlock corresponds to a step sequence $\theta \in \text{infsts}(N)$ such that it can be decomposed as $\theta = \theta' \cdot \emptyset \emptyset \dots$ and $\{\theta'' \mid \theta' \cdot \theta'' \in \text{infsts}(N)\} = \emptyset^\omega$.

etc). For two syntax paths, π_1 and π_2 , we use $\pi_1|\pi_2$ to denote that $\{\pi_1, \pi_2\} = \{\pi \cdot \text{!}_L \cdot \pi', \pi \cdot \text{!}_R \cdot \pi''\}$, for some π , π' , and π'' . Intuitively, two actions of a non-synchronised box expression are concurrent iff their positions π_1 and π_2 in the syntax (or parse) tree are such that $\pi_1|\pi_2$.

The form of each place in composite boxes will be p_Z , where $p \in \{e, i, x\}$ and $Z \subseteq \Pi \cdot \{e, x\}$, while each transition will be of the form a_W , where $a \in \mathcal{A}$ and $W \subseteq \Pi$. Moreover, for brevity, the sets Z and W will be written as comma-separated lists without brackets.

For a syntax path $\pi \in \Pi$ and a transition a_W , we denote $\pi \cdot a_W = a_{\pi \cdot W}$. This prefix notation extends in the usual way to sets of transitions and sequences of sets of transitions as well as (sets of) places.

The specific form of places and transitions, together with the systematic way in which boxes are manipulated below, will mean that for a compositional box $N = (P, T, F, \ell)$ it will be the case that, for all $p_Z \in P$ and $a_W \in T$, $\ell(p_Z) = p$ and $\ell(a_W) = a$, as well as:

$$\begin{aligned} (p_Z, a_W) \in F &\iff \exists \pi \in W : \pi \cdot e \in Z \\ (a_W, p_Z) \in F &\iff \exists \pi \in W : \pi \cdot x \in Z. \end{aligned} \tag{1}$$

Thus both the flow relation and labelling function are implicit, and we will represent such a box simply by $N = (P, T)$.

We will now present a systematic way of constructing composite boxes from box expressions, i.e., for each constant expression we define a box and, for each operator used in box expressions, a corresponding operator on boxes.

Constants With the blocking expression stop and a single-action expression $a \in \mathcal{A}$, we respectively associate the following boxes:

$$N_{\text{stop}} = (\{e_e, x_x\}, \emptyset) \quad \text{and} \quad N_a = (\{e_e, x_x\}, \{a_e\}). \tag{2}$$

Their diagrams are depicted in Figure 3 with labels shown inside the nodes. N_{stop} consists of one entry place, one exit place, and nothing else. N_a contains, in addition, a single transition. The way it is connected with the two places is determined by the formula (1). For example, $(e_e, a_e) \in F$ since $\pi = \epsilon$ is an annotation of a_e and $\pi \cdot e = e$ is an annotation of e_e .

Sequence $N ; K$ combines the exit interface of N with the entry interface of K . The entry interface of the resulting box is that of N , and the exit interface is that of K . For an example see the diagram of $N_a ; N_b$ in Figure 3.

$$\begin{aligned} N ; K &= (P_L \cup P_R \cup \mathcal{X}, T_L \cup T_R) \\ \text{where } \begin{cases} T_L &= ;_L \cdot N^\top & T_R &= ;_R \cdot K^\top \\ P_L &= ;_L \cdot N^{\text{ei}} & P_R &= ;_R \cdot K^{\text{ix}} \\ \mathcal{X} &= \{i;_L \cdot Z \cup ;_R \cdot W \mid x_Z \in N^x \wedge e_W \in K^e\}. \end{cases} \end{aligned}$$

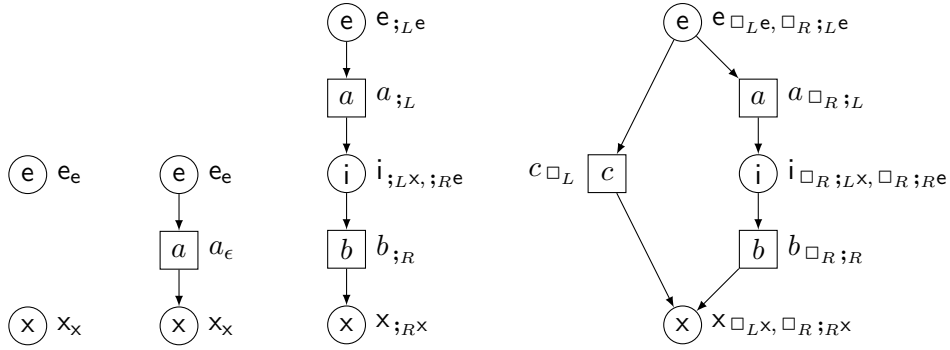


Figure 3. From the left to right: diagrams of N_{stop} and N_a and $N_a ; N_b$ and $N_c \square (N_a ; N_b)$. The last two boxes correspond to the box expressions $a ; b$ and $c \square (a ; b)$.

Non-deterministic choice $N \square K$ combines together the entry interfaces of the two boxes creating a new entry interface, as well as their exit interfaces creating a new exit interface. For an example, see the diagram of $N_c \square (N_a ; N_b)$ in Figure 3.

$$N \square K = (P_L \cup P_R \cup \mathcal{X} \cup \mathcal{Y}, T_L \cup T_R)$$

$$\text{where } \begin{cases} T_L = \square_L \cdot N^T & T_R = \square_R \cdot K^T \\ P_L = \square_L \cdot N^i & P_R = \square_R \cdot K^i \\ \mathcal{X} = \{e_{\square_L \cdot Z \cup \square_R \cdot W} \mid e_Z \in N^e \wedge e_W \in K^e\} \\ \mathcal{Y} = \{x_{\square_L \cdot Z \cup \square_R \cdot W} \mid x_Z \in N^x \wedge x_W \in K^x\}. \end{cases}$$

Parallelism $N \parallel K$ simply puts the boxes N and K next to each other. The new entry (resp., exit) interface is the union of the entry (resp., exit) interfaces of the composed boxes.

$$N \parallel K = (P_L \cup P_R, T_L \cup T_R)$$

$$\text{where } \begin{cases} T_L = \parallel_L \cdot N^T & T_R = \parallel_R \cdot K^T \\ P_L = \parallel_L \cdot N^P & P_R = \parallel_R \cdot K^P \end{cases}$$

Iteration $\llbracket N \circledast K \circledast J \rrbracket$ combines the exit interfaces of N and K with the entry interfaces of K and J , respectively. For an example see the diagram of $\llbracket N_a \circledast (N_b \parallel N_c) \circledast N_d \rrbracket$ in Figure 4(a). The

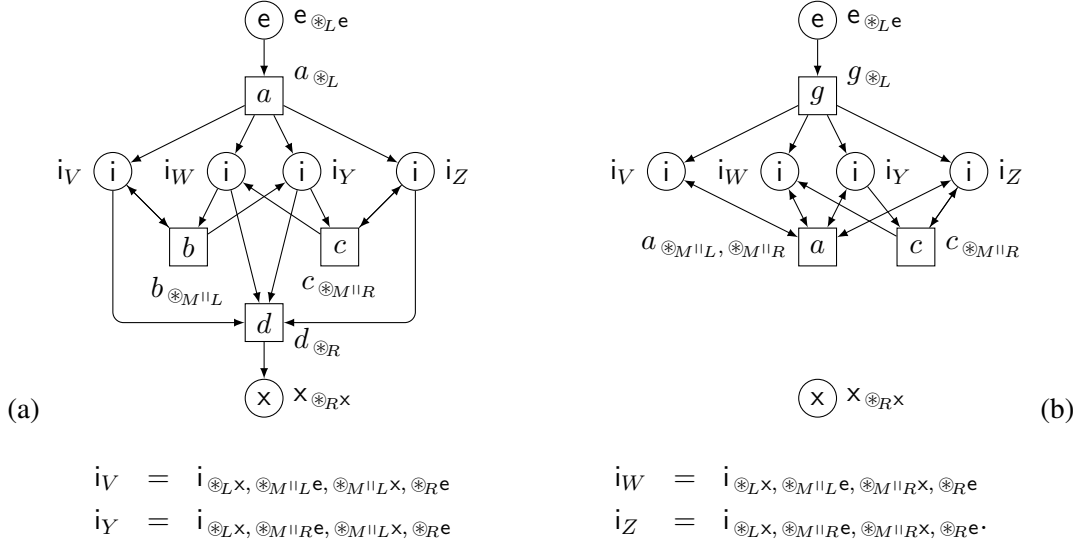


Figure 4. Diagrams of two boxes involving parallel composition and iteration: $\llbracket N_a \otimes (N_b \parallel N_c) \otimes N_d \rrbracket$ (a), and $(\llbracket N_a \otimes (N_b \parallel N_c) \otimes N_d \rrbracket) \text{ sco } \{a \mapsto g, bc \mapsto a, c \mapsto c\}$ (b). The four internal places are defined below the diagrams.

new entry interface is that of N , and the exit interface is that of J .

$$\llbracket N \otimes K \otimes J \rrbracket = (P_L \cup P_M \cup P_R \cup \mathcal{X}, T_L \cup T_M \cup T_R)$$

$$\text{where } \begin{cases} T_L = \otimes_L \cdot N^\top & T_M = \otimes_M \cdot K^\top & T_R = \otimes_R \cdot J^\top \\ P_L = \otimes_L \cdot N^{\text{ei}} & P_M = \otimes_M \cdot K^{\text{i}} & P_R = \otimes_R \cdot J^{\text{ix}} \\ \mathcal{X} = \{i \otimes_L \cdot Z \cup \otimes_M \cdot W \cup \otimes_M \cdot V \cup \otimes_R \cdot Y \mid \\ \quad x_Z \in N^\times \wedge e_W \in K^e \wedge x_V \in K^\times \wedge e_Y \in J^e\}. \end{cases}$$

At this point it is possible to formulate a useful result which holds for all boxes which can be constructed using the rules defined so far (clearly, any net constructed in this way is a box).

Proposition 2.1. Let N be any net constructed from boxes corresponding to the constant box expressions as well as the operators for sequence, choice, parallelism, and iteration. Then, the set of steps appearing in the step sequences of N is included in the following set:

$$N^{\text{psteps}} = \left\{ \{a_{\pi_1}^1, \dots, a_{\pi_n}^n\} \subseteq N^\top \mid \forall i < j : \pi_i | \pi_j \right\}.$$

Moreover, if the net N_{stop} defined in (2) is not used in the construction, then N^{psteps} is exactly the set of steps appearing in the step sequences of N .

Proof:

Follows by structural induction from the general results proved, e.g., in [13] for Box Algebra models. \square

The last result states that the valid steps of N (i.e., those occurring in the step sequences of N) do not contain conflicting or causally dependent transitions, i.e., transitions whose syntactic paths π and π' do not satisfy $\pi|\pi'$. Henceforth we will call N^{psteps} the set of potential steps of N . Moreover, for every synchronisation relation ρ , we define:

$$\rho_N = \{(U, a_{\pi_1, \dots, \pi_n}) \mid U = \{a_{\pi_1}^1, \dots, a_{\pi_n}^n\} \in N^{\text{psteps}} \wedge (a^1, \dots, a^n, a) \in \rho\}.$$

Each pair $(U, a_W) \in \rho_N$ comprises a set of transitions U which in principle might occur simultaneously in a valid step of N and, under the synchronisation specified by ρ in $N \text{ sco } \rho$, would give rise to a compound transition a_W . For example, if we take the box N in Figure 4(a) with the synchronisation relation $\rho = \{a \mapsto g, bc \mapsto a, c \mapsto c\}$, then we have:

$$\begin{aligned} N^{\text{psteps}} &= \{\emptyset, \{a_{\oplus_L}\}, \{b_{\oplus_{M \parallel L}}\}, \{c_{\oplus_{M \parallel R}}\}, \{d_{\oplus_R}\}, \{b_{\oplus_{M \parallel L}}, c_{\oplus_{M \parallel R}}\}\} \\ \rho_N &= \{(\{a_{\oplus_L}\}, g_{\oplus_L}), (\{c_{\oplus_{M \parallel R}}\}, c_{\oplus_{M \parallel R}}), (\{b_{\oplus_{M \parallel L}}, c_{\oplus_{M \parallel R}}\}, a_{\oplus_{M \parallel L}, \oplus_{M \parallel R}})\}. \end{aligned}$$

Scoping $N \text{ sco } \rho$ has the same places as N and, for each potential step of N , one creates a new transition representing a synchronisation of two or more actions of N , if the potential step is not a singleton. After that, all the transitions of N are removed. Formally,

$$N \text{ sco } \rho = (N^{\text{P}}, \mathcal{Z})$$

$$\text{where } \mathcal{Z} = \{t \mid \exists U : (U, t) \in \rho_N\}.$$

As an example, the diagram of $\llbracket N_a \otimes (N_b \parallel N_c) \otimes N_d \rrbracket \text{ sco } \{a \mapsto g, bc \mapsto a, c \mapsto c\}$ is depicted in Figure 4(b).

As argued in [13], suitable synchronisation relations can capture a wide range of synchronisation schemes of two or more actions (e.g., $bc \mapsto a$ above), as well as action relabelling (e.g., $a \mapsto g$ and $c \mapsto c$) and action restriction (as with d).

2.4. From expressions to boxes

We can now define the semantics of box expressions by transforming them compositionally into the corresponding box nets, and then adopting the execution semantics of the latter. Formally, we define a mapping $\text{box}(\cdot)$ from expressions to boxes, in the following way:

$$\begin{aligned} \text{box}(\text{stop}) &= N_{\text{stop}} \\ \text{box}(a) &= N_a \\ \text{box}(E ; E') &= \text{box}(E) ; \text{box}(E') \\ \text{box}(E \square E') &= \text{box}(E) \square \text{box}(E') \\ \text{box}(E \parallel E') &= \text{box}(E) \parallel \text{box}(E') \\ \text{box}(\llbracket E \otimes E' \otimes E'' \rrbracket) &= \llbracket \text{box}(E) \otimes \text{box}(E') \otimes \text{box}(E'') \rrbracket \\ \text{box}(E \text{ sco } \rho) &= \text{box}(E) \text{ sco } \rho \end{aligned} \tag{3}$$

From now on, by a box we will mean a composite box constructed using (3). According to the BA theory, such boxes enjoy a number of interesting behavioural properties when we consider executions starting from their initial markings as follows (the facts listed below follow from the general results proved in [13] for more Box Algebra models):

- The number of tokens on any place for any reachable marking is bounded; more precisely, the number of tokens on any internal place is never greater than two (i.e., the internal places are 2-bounded), and an entry or exit place never holds more than one token (i.e., the entry and exit places are 1-bounded).
- Each box is clean, which means that when all the exit places contain tokens, there is no token left elsewhere in the net.
- Boxes do not allow auto-concurrency, which means that there is no transition enabled ‘twice’ for any reachable marking. As a consequence, steps can be represented by sets rather than by multiset of transitions.

The complexity of semantical representations of box expressions provided by the $\text{box}(\cdot)$ mapping is in the worst case exponential. To show this, let us consider a synchronised expression $F = E \text{ sco } \rho$. We define its size $|F|$ as $|E| + |\rho|$, where $|E|$ is the size of E taken to be the total number of occurrences of the stop’s and a ’s within E , and $|\rho|$ to be the total number of action occurrences within ρ . Moreover, we define the size of a box N as $|N| = |N^P| + |N^T| + |N^F|$. We first observe that $|\text{box}(E)^P|$, $|\text{box}(E)^T|$, and $|\text{box}(E)^F|$ all belong to $O(2^{|E|})$. Hence $|\text{box}(E)| \in O(2^{|E|})$. As far as F is concerned, we observe that $|\text{box}(F)^P| = |\text{box}(E)^P| \in O(2^{|E|})$, $|\text{box}(F)^T| \in O(2^{|E|} \cdot |\rho|)$, and so $|\text{box}(F)^F|$ also belongs to $O(2^{|E|} \cdot |\rho|)$. Hence $|\text{box}(F)| \in O(2^{|E|} \cdot |\rho|)$. These estimates cannot be much improved which can be shown by taking a sequence of synchronised box expressions $F_n = E_n \text{ sco } \rho_n$ where, for every $n \geq 1$:

$$E_n = \underbrace{((b \parallel b) \square \dots \square (b \parallel b))}_{n \text{ times}} \parallel \underbrace{(a \parallel \dots \parallel a)}_{n \text{ times}} \quad \text{and} \quad \rho_n = \{b \mapsto b\} \cup \underbrace{\{a \dots a \mapsto a \mid 1 \leq k \leq n\}}_{k \text{ times}}.$$

We first observe that $|E_n| = 3 \cdot n$, $|\rho_n| = 2 + \frac{1}{2} \cdot n \cdot (n + 3)$, and $|F_n| = 4 \cdot n + 1$. On the other hand:

$$\begin{aligned} |\text{box}(E_n)^P| &= 2^{n+1} & + & 2 \cdot n & |\text{box}(E_n)^P| &= 2^{n+1} & + & 2 \cdot n \\ |\text{box}(E_n)^T| &= 2 \cdot n & + & n & |\text{box}(F_n)^T| &= 2 \cdot n & + & 2^n - 1 \\ |\text{box}(E_n)^F| &= n \cdot 2^{n+1} & + & 2 \cdot n & |\text{box}(F_n)^F| &= n \cdot 2^{n+1} & + & \sum_{k=1}^n k \cdot \binom{n}{k}. \end{aligned}$$

2.5. Behavioural properties of composite boxes

Step sequences of composite boxes exhibit compositional properties [13], i.e., one can (easily) derive the semantics of a composite box from the semantics of the boxes being composed. This is demonstrated by a series of results which follow from the general properties of boxes [13].

For boxes modelling the blocking expression and a single-action expression, the semantics capture is straightforward.

Proposition 2.2. (basic boxes)

The following hold, where $a \in \mathcal{A}$:

$$\begin{aligned} \text{infsts}(N_{\text{stop}}) &= \emptyset^\omega & \text{infsts}(N_a) &= \emptyset^\omega \cup \emptyset^* \cdot \{\{a_\epsilon\}\} \cdot \emptyset^\omega \\ \text{finsts}(N_{\text{stop}}) &= \emptyset & \text{finsts}(N_a) &= \emptyset^* \cdot \{\{a_\epsilon\}\} \cdot \emptyset^*. \end{aligned}$$

Proof:

Follows from the general results proved, e.g., in [13]. \square

For choice and parallelism, the semantics of a composite box can easily be expressed in terms of the semantics of the composed boxes. We need, however, a notion of parallel composition of step sequences.

For two step sequences, θ and δ , of equal length, $\theta \parallel \delta$ is a step sequence of the same length as θ and δ , and $(\theta \parallel \delta)_{(k)} = \theta_{(k)} \cup \delta_{(k)}$, for all $k \leq |\delta| = |\theta|$. Moreover, for two sets of step sequences, Θ and Δ , $\Theta \parallel \Delta = \{\theta \parallel \delta \mid \theta \in \Theta \wedge \delta \in \Delta \wedge |\delta| = |\theta|\}$.

Proposition 2.3. (choice and parallelism)

The following hold, where $sts = \text{infsts}$ or $sts = \text{finsts}$:

$$\begin{aligned} sts(N \sqcap K) &= \sqcap_L \cdot sts(N) \cup \sqcap_R \cdot sts(N) \\ sts(N \parallel K) &= \parallel_L \cdot sts(N) \parallel \parallel_R \cdot sts(N). \end{aligned}$$

Proof:

Follows from the general results proved, e.g., in [13]. \square

Proposition 2.4. (sequence)

The following hold:

$$\begin{aligned} \text{infsts}(N ; K) &= ;_L \cdot \text{finsts}(N) \cdot ;_R \cdot \text{infsts}(K) \cup ;_L \cdot \text{infsts}(N) \\ \text{finsts}(N ; K) &= ;_L \cdot \text{finsts}(N) \cdot ;_R \cdot \text{finsts}(K). \end{aligned}$$

Proof:

Follows from the general results proved, e.g., in [13]. \square

Proposition 2.5. (iteration)

The following hold:

$$\begin{aligned} \text{infsts}(\llbracket N \circledast K \circledast J \rrbracket) &= \circledast_L \cdot \text{infsts}(N) \cup \\ &\quad \circledast_L \cdot \text{finsts}(N) \cdot (\circledast_M \cdot \text{finsts}(K))^* \cdot \circledast_M \cdot \text{infsts}(K) \cup \\ &\quad \circledast_L \cdot \text{finsts}(N) \cdot (\circledast_M \cdot \text{finsts}(K))^* \cdot \circledast_R \cdot \text{infsts}(J) \\ \text{finsts}(\llbracket N \circledast K \circledast J \rrbracket) &= \circledast_L \cdot \text{finsts}(N) \cdot (\circledast_M \cdot \text{finsts}(K))^* \cdot \circledast_R \cdot \text{finsts}(J). \end{aligned}$$

Proof:

Follows from the general results proved, e.g., in [13]. \square

Finally, we consider a box $N \text{ sco } \rho$, where N is constructed from some non-synchronised box expression. In this case, relating step sequences of $N \text{ sco } \rho$ and N is more involved.

First, we define a relation $\tilde{\rho}_N$ comprising all pairs $(U, \{t_1, \dots, t_k\})$ ($k \geq 0$), where $U \in N^{\text{psteps}}$ and $\{t_1, \dots, t_k\} \subseteq \text{box}(N \text{ sco } \rho)^\top$ are such that there is a partition U_1, \dots, U_k of U satisfying $(U_j, t_j) \in \rho_N$, for each $j \leq k$. Moreover, for two equal length sequences of sets of transitions, τ and θ , we denote $(\tau, \theta) \in \tilde{\rho}_N$ if $(\tau_{(j)}, \theta_{(j)}) \in \tilde{\rho}_N$, for all j .

Proposition 2.6. (scoping)

$\text{sts}(N \text{ sco } \rho) = \{\theta \mid \exists \tau \in \text{sts}(N) : (\tau, \theta) \in \tilde{\rho}_N\}$, where $\text{sts} = \text{infsts}$ or $\text{sts} = \text{finsts}$.

Proof:

Follows from the general results proved, e.g., in [13]. □

2.6. Streamlined box expressions

The translation from BA to ITL, that will be described in Section 4, is particularly simple for the class of streamlined synchronised expressions.

We first observe that each transition a_W in the box associated with a non-synchronised box expression is such that W is a singleton. Thus, a_W is represented as a_π for a syntax path π .

We call a box expression $E \text{ sco } \rho$ *streamlined* if for each transition $a_\pi \in \text{box}(E)^\top$ there is exactly one transition $b_W \in \text{box}(E \text{ sco } \rho)^\top$ such that $\pi \in W$. That is, any pre-synchronisation action in a streamlined box expression contributes to *exactly one* action after applying the synchronisation.³

We will now demonstrate that each synchronised box expression $F = E \text{ sco } \rho$ can be transformed into a semantically equivalent⁴ streamlined expression $\text{stl}(F) = F' = E' \text{ sco } \rho'$. First, for every $a_\pi \in \text{box}(E)^\top$, let

$$\text{trans}(a_\pi) = \{b_W \in \text{box}(F)^\top \mid \pi \in W\} = \{t \mid \exists (U, t) \in \rho_{\text{box}(E)} : a_\pi \in U\}.$$

In other words, $\text{trans}(a_\pi)$ comprises all post-synchronisation transitions in which a_π has been involved. For example, if we take $\llbracket a \otimes (b \parallel c) \otimes d \rrbracket \text{ sco } \{a \mapsto g, bc \mapsto a, c \mapsto c\}$ with the corresponding box depicted in Figure 4(b), we have:

$$\begin{aligned} \text{trans}(a_{\otimes_L}) &= \{g_{\otimes_L}\} & \text{trans}(b_{\otimes_{M \parallel L}}) &= \{a_{\otimes_{M \parallel L}}, \otimes_{M \parallel L}\} \\ \text{trans}(d_{\otimes_R}) &= \emptyset & \text{trans}(c_{\otimes_{M \parallel R}}) &= \{a_{\otimes_{M \parallel L}}, \otimes_{M \parallel R}, c_{\otimes_{M \parallel R}}\} \end{aligned}$$

Then, a suitable E' is obtained by replacing each occurrence of an action $a \in \mathcal{A}$ in E corresponding to transition a_π in $\text{box}(E)$ ⁵ by:

- stop if we have $\text{trans}(a_\pi) = \emptyset$,
- b_W if we have $\text{trans}(a_\pi) = \{b_W\}$, and
- $b_{W_1}^1 \sqcap (\dots \sqcap (b_{W_{m-1}}^{m-1} \sqcap b_{W_m}^m) \dots)$ if we have $\text{trans}(a_\pi) = \{b_{W_1}^1, \dots, b_{W_m}^m\}$ and $m \geq 2$ ⁶.

³As a result, each pre-synchronisation action can be represented by a single variable in the corresponding ITL formula.

⁴In the sense of generating isomorphic box net with ‘isomorphic’ step sequences, see Proposition 2.7.

⁵Such an occurrence of a is identified by the path in the syntax tree of the non-synchronised expression E which corresponds to π .

⁶We assume a fixed ordering on the transitions of $\text{box}(F)$ so that the enumeration of $\text{trans}(a_\pi)$ is unique.

Furthermore, we modify the scoping part reflecting changes affecting the set of actions:

$$\rho' = \underbrace{\{b_W \dots b_W \mapsto b_W \mid b_W \in \text{box}(F)^\top\}}_{|W| \text{ times}}$$

The $b_W \dots b_W \mapsto b_W$ above reflects the fact that b_W is constructed as a synchronisation of $|W|$ actions in $\text{box}(E)$, each such action being now replaced by a copy of b_W in E' .

Continuing the last example, we obtain $F' = E' \text{ sco } \rho'$, with

$$E' = (\llbracket \gamma \circledast (\alpha \parallel (\alpha \sqcap \zeta)) \circledast \text{stop} \rrbracket) \quad \text{and} \quad \rho' = \{\gamma \mapsto \gamma, \alpha \mapsto \alpha, \zeta \mapsto \zeta\},$$

where $\gamma = g \circledast_L$, $\alpha = a \circledast_{M \parallel L, \circledast_{M \parallel R}}$, and $\zeta = c \circledast_{M \parallel R}$.

Each transition of $\text{box}(F')$ is of the form $(b_W)_Y$, where $b_W \in \text{box}(E \text{ sco } \rho)^\top$,⁷ and we then define a bijection $\lambda : \text{box}(F')^\top \rightarrow \text{box}(F)^\top$ by setting $\lambda((b_W)_Y) = b_W$. Such a λ can be applied in the standard way to the subsets of $\text{box}(F')^\top$ and their sequences.

Proposition 2.7. (streamlined expression)

The nets $\text{box}(F)$ and $\text{box}(\text{stl}(F))$ are isomorphic after replacing each transition label b_W in $\text{box}(\text{stl}(F))$ by b . Moreover, $\text{sts}(\text{box}(F)) = \lambda(\text{sts}(\text{box}(\text{stl}(F))))$, where $\text{sts} = \text{infsts}$ or $\text{sts} = \text{finsts}$.

Proof:

We observe that the sets of places in $\text{box}(F)$ and $\text{box}(\text{stl}(F))$ are in a one-to-one correspondence since the subexpressions of the form stop or b_W or $b_{W_1}^1 \sqcap (\dots \sqcap (b_{W_{m-1}}^{m-1} \sqcap b_{W_m}^m) \dots)$ produce boxes with exactly one entry place, one exit place, and no internal places.

The first part of the result follows from the fact that the result of synchronising $|W|$ transitions in $\text{box}(E)$ to yield a transition b_W is the same w.r.t. connections with the (corresponding) places as the result of synchronising $|W|$ transitions labelled b_W in $\text{box}(E')$ yielding a transition $(b_W)_Y$.

The second part follows from the first part and the fact that λ is a part of isomorphism between $\text{box}(F)$ and $\text{box}(\text{stl}(F))$. \square

Streamlined expressions will prove their usefulness in the translation of box expressions to behaviourally equivalent ITL formulas. Intuitively, they allow one to separate the roles that a single variable can play in different synchronisation contexts.

In terms of complexity, the transformation of $F = E \text{ sco } \rho$ into a streamlined $\text{stl}(F) = E' \text{ sco } \rho'$ will usually result in a larger expression. This increase is a result of inserting sub-expressions of the form $b_{W_1}^1 \sqcap (\dots \sqcap (b_{W_{m-1}}^{m-1} \sqcap b_{W_m}^m) \dots)$ and modifying the scoping relation. It is straightforward to check that $|E'| \in O(|E| + |\text{box}(E)^F|)$ and $|\rho'| = |\text{box}(E)^F|$. Hence $|\text{stl}(F)| \in O(|E| + |\text{box}(E)^F|)$, and so $|\text{stl}(F)| \in O(|E| + |\text{box}(E)|)$. Since it can be argued that, for the purpose of behavioural analyses within the domain of Petri nets, the problem size is $|F| + |\text{box}(F)|$, the transformation to streamlined expressions is efficient.

It is also important to stress that it is not necessary to derive $\text{box}(F)$ in order to derive $\text{stl}(F)$.

⁷In general, $W \neq Y$ since the syntax tree of E is not isomorphic to the syntax tree of E' due to the introduction of sub-expressions of the form $b_{W_1}^1 \sqcap (\dots \sqcap (b_{W_{m-1}}^{m-1} \sqcap b_{W_m}^m) \dots)$.

3. Interval Temporal Logic

We now provide the syntax and semantics of a small fragment of ITL, including only those constructs (basic and derived) which are used in the translation of box expressions. In particular, we assume that Var is a countable set of Boolean variables, all such variables being the *transitions* of boxes created using the box mapping.

The formulas of the fragment of the ITL logic we need are defined by:

$$\varphi ::= \text{true} \mid \text{flip}(v) \mid \text{skipstbl}(v) \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \varphi ; \varphi \mid \varphi^* \mid \text{inf}$$

where $v \in Var$. Intuitively, $\text{flip}(v)$ inverts the value of a Boolean variable v over a unit interval, $\text{skipstbl}(v)$ keeps unchanged the value of v over a unit interval, “;” is a sequential composition operator (called chop), “*” is an iterative version of chop (called chop-star), and inf indicates an infinite interval. The set of variables occurring in a formula φ is denoted by $\text{var}(\varphi)$. and its size $|\varphi|$ is the total number of occurrences of the v ’s, stop’s, and inf ’s within φ .

Remark 3.1. The logic syntax introduced above has been tailored to smooth the translation from box expression to logical formulas. However, all the non-standard constructs used (i.e., true , flip , skipstbl and inf) can be expressed in the standard ITL logic, in the following way:

$$\begin{aligned} \text{true} &= v \vee \neg v & \text{flip}(v) &= (\bigcirc \neg \bigcirc \text{true}) \wedge (v = \bigcirc \neg v) \\ \text{inf} &= \text{true} ; \neg \text{true} & \text{skipstbl}(v) &= (\bigcirc \neg \bigcirc \text{true}) \wedge (v = \bigcirc v) \end{aligned}$$

where \bigcirc is the temporal operator ‘next’. Hence it is possible to formulate and analyse behavioural properties of any translated formula using proof techniques and tools of ITL (e.g., [11] gives a complete axiomatisation of both finite and infinite time). \diamond

A *state* s is a mapping which assigns values to the Boolean variables Var , and an *interval* σ is a non-empty sequence of states. The meaning of formulas is given by the satisfaction relation \models involving intervals and formulas, defined as follows (below s, s' are states, and $\sigma, \sigma', \sigma''$ are intervals):

- $\sigma \models \text{true}$,
- $\sigma \models \text{flip}(v)$ if $\sigma = s \cdot s'$ and $s(v) \neq s'(v)$,
- $\sigma \models \text{skipstbl}(v)$ if $\sigma = s \cdot s'$ and $s(v) = s'(v)$,
- $\sigma \models \varphi \vee \varphi'$ if $\sigma \models \varphi$ or $\sigma \models \varphi'$,
- $\sigma \models \varphi \wedge \varphi'$ if $\sigma \models \varphi$ and $\sigma \models \varphi'$,
- $\sigma \models \varphi ; \varphi'$ if σ is infinite and $\sigma \models \varphi$, or σ can be decomposed as $\sigma = \sigma' \cdot s \cdot \sigma''$ so that $\sigma' \cdot s \models \varphi$ and $s \cdot \sigma'' \models \varphi'$,
- $\sigma \models \varphi^*$ if $|\sigma| = 1$, or σ can be decomposed as $\sigma = \sigma' \cdot s \cdot \sigma''$ so that $\sigma' \cdot s \models \varphi$ and $s \cdot \sigma'' \models \varphi^*$, and

- $\sigma \models \text{inf}$ if σ is infinite.

To capture the relationship between the semantics of a box expression and a corresponding formula, with each ITL formula φ and interval σ satisfying $\sigma \models \varphi$, we associate a sequence of sets $\mu_\sigma = \Gamma_1 \Gamma_2 \dots$, where, for each $j < |\sigma|$, Γ_j is given by:

$$\Gamma_j = \text{var}(\varphi) \cap \{v \in \text{var}(\varphi) \mid \sigma(j)(v) \neq \sigma(j+1)(v)\}.$$

Since each Γ_j records all the variables which flipped their values at the point of entering the state $\sigma(j+1)$, the sequence μ_σ provides a direct interpretation of σ in terms of sequences of steps of transitions of box nets. Then, for any ITL formula φ , we define:

$$\text{infsts}(\varphi) = \{\mu_\sigma \mid \sigma \models \varphi \wedge |\sigma| = \omega\} \quad \text{and} \quad \text{finsts}(\varphi) = \{\mu_\sigma \mid \sigma \models \varphi \wedge |\sigma| < \omega\}.$$

Below we present a number of semantrical properties of ITL formulas considered in this paper.

Proposition 3.2. Let φ and φ' be two formulas with disjoint sets of variables, i.e., $\text{var}(\varphi) \cap \text{var}(\varphi') = \emptyset$. Then, the following hold, where $\psi = \text{skipstbl}(\text{var}(\varphi))^*$, $\psi' = \text{skipstbl}(\text{var}(\varphi'))^*$, and $\text{sts} = \text{infsts}$ or $\text{sts} = \text{finsts}$:

$$\begin{aligned} \text{sts}((\varphi \wedge \psi') \vee (\varphi' \wedge \psi)) &= \text{sts}(\varphi) \cup \text{sts}(\varphi') \\ \text{sts}(\varphi \wedge \varphi') &= \text{sts}(\varphi) \parallel \text{sts}(\varphi') \\ \text{infsts}((\varphi \wedge \psi') ; (\varphi' \wedge \psi)) &= \text{infsts}(\varphi) \cup \text{finsts}(\varphi) \cdot \text{infsts}(\varphi') \\ \text{finsts}((\varphi \wedge \psi') ; (\varphi' \wedge \psi)) &= \text{finsts}(\varphi) \cdot \text{finsts}(\varphi'). \end{aligned}$$

Proof:

Follows directly from the basic properties of logic operators. □

Proposition 3.3. Let φ_i , for $i = 1, 2, 3$, be formulas with mutually disjoint sets of variables. Then, the following hold, where $\psi_{i,j} = \text{skipstbl}(\text{var}(\varphi_i) \cup \text{var}(\varphi_j))^*$, for $i, j \in \{1, 2, 3\}$:

$$\begin{aligned} \text{finsts}((\varphi_1 \wedge \psi_{2,3}) ; ((\varphi_2 \wedge \psi_{1,3})^* ; (\varphi_3 \wedge \psi_{1,2}))) &= \text{finsts}(\varphi_1) \cdot \text{finsts}(\varphi_2)^* \cdot \text{finsts}(\varphi_3) \\ \text{infsts}((\varphi_1 \wedge \psi_{2,3}) ; ((\varphi_2 \wedge \psi_{1,3})^* ; (\varphi_3 \wedge \psi_{1,2}))) &= \text{infsts}(\varphi_1) \cup \\ &\quad \text{finsts}(\varphi_1) \cdot \text{finsts}(\varphi_2)^* \cdot (\text{infsts}(\varphi_2) \cup \text{infsts}(\varphi_3)). \end{aligned}$$

Proof:

We obtain the following, after noting that $\text{var}(\varphi_2^*) = \text{var}(\varphi_2)$ and twice using Proposition 3.2:

$$\begin{aligned} &\text{finsts}((\varphi_1 \wedge \psi_{2,3}) ; ((\varphi_2 \wedge \psi_{1,3})^* ; (\varphi_3 \wedge \psi_{1,2}))) \\ &= \text{finsts}((\varphi_1 \wedge \psi_{2,3}) ; (((\varphi_2 \wedge \psi_{3,3})^* ; (\varphi_3 \wedge \psi_{2,2})) \wedge \psi_{1,1})) \\ &= \text{finsts}(\varphi_1) \cdot \text{finsts}((\varphi_2 \wedge \psi_{3,3})^* ; (\varphi_3 \wedge \psi_{2,2})) \\ &= \text{finsts}(\varphi_1) \cdot \text{finsts}((\varphi_2^* \wedge \psi_{3,3}) ; (\varphi_3 \wedge \text{skipstbl}(\text{var}(\varphi_2^*))^*)) \\ &= \text{finsts}(\varphi_1) \cdot \text{finsts}(\varphi_2^*) \cdot \text{finsts}(\varphi_3) \\ &= \text{finsts}(\varphi_1) \cdot \text{finsts}(\varphi_2)^* \cdot \text{finsts}(\varphi_3). \end{aligned}$$

The second part of the proof is similar. □

Proposition 3.4. Let φ' be a formula obtained from an ITL formula φ by a consistent renaming of Boolean variables given by a bijection λ . Then $sts(\varphi') = \lambda(sts(\varphi))$, for $sts = \text{infsts}$ or $sts = \text{finsts}$.

Proof:

Follows from the insensitivity of \models to the identities of logic variables. \square

For a formula φ and $\pi \in \Pi$, we will denote by $\pi \cdot \varphi$ the formula obtained from φ by replacing each variable $a_{\pi'}$ with $a_{\pi \cdot \pi'}$.

Proposition 3.5. If $\pi \in \Pi$ then $sts(\pi \cdot \varphi) = \pi \cdot sts(\varphi)$, where $sts = \text{infsts}$ or $sts = \text{finsts}$.

Proof:

This follows from Proposition 3.4 and the fact that the transformation given by $\pi \cdot \varphi$ is a consistent renaming of variables. \square

3.1. Derived formulas

and we will use the following derived formulas, for finite sets of variables $V, V' \subseteq \text{Var}$:

$$\begin{aligned}
 \text{skipstbl}(V) &= \begin{cases} \bigwedge_{v \in V} \text{skipstbl}(v) & \text{if } V \neq \emptyset \\ \text{true} & \text{otherwise} \end{cases} \\
 \text{infstbl}(V) &= \text{inf} \wedge \text{skipstbl}(V)^* \\
 \text{flip}(V) &= \begin{cases} \bigvee_{v \in V} \text{flip}(v) \wedge \text{skipstbl}(V \setminus \{v\}) & \text{if } V \neq \emptyset \\ \text{inf} & \text{otherwise} \end{cases} \\
 \text{fs}(V \mid V') &= \text{skipstbl}(V \cup V')^* ; (\text{flip}(V) \wedge \text{skipstbl}(V')) ; \text{skipstbl}(V \cup V')^*.
 \end{aligned} \tag{4}$$

Intuitively, $\text{skipstbl}(V)$ and $\text{infstbl}(V)$ keep unchanged the values of the variables in V respectively over a unit and infinite interval, $\text{flip}(V)$ inverts the value of exactly one of the Boolean variables in V over a unit interval, and $\text{fs}(V \mid V')$ keeps unchanged the values of the variables in V and V' over an interval except that it inverts the value of exactly one variable in V over a unit sub-interval. We can drop the set parenthesis when writing down the above formulas.

Using the derived syntax, we call an ITL logical formula an *infstbl/fs*-formula if it constructed using $\text{infstbl}(V)$'s, $\text{fs}(V \mid V')$'s and the four logical operators of the syntax. The translation we will introduce in the next section will translate box expressions into *infstbl/fs*-formulas.

It is easily checked that the following hold for any finite sets of variables $V, V', V'' \subseteq \text{Var}$ satisfying $V \cap V'' = \emptyset$, where \equiv denotes the standard equivalence of formulas w.r.t. the satisfaction relation \models :

$$\begin{aligned}
 \text{fs}(\emptyset \mid V) &\equiv \text{infstbl}(V) & \text{infstbl}(V) \wedge \text{skipstbl}(V')^* &\equiv \text{infstbl}(V \cup V') \\
 \text{fs}(V \mid V) &\equiv \text{infstbl}(V) & \text{fs}(V \mid V') \wedge \text{skipstbl}(V'')^* &\equiv \text{fs}(V \mid V' \cup V'').
 \end{aligned} \tag{5}$$

Moreover, $\text{skipstbl}(V)$ distributes over the logical operators as we have the following, for all formulas φ, φ' and finite sets of variables V :

$$\begin{aligned} (\varphi \wedge \varphi') \wedge \text{skipstbl}(V) &\equiv (\varphi ; \varphi') \wedge \text{skipstbl}(V) \\ (\varphi \wedge \varphi') \vee \text{skipstbl}(V) &\equiv (\varphi \wedge \text{skipstbl}(V)) \vee (\varphi' \wedge \text{skipstbl}(V)) \\ (\varphi \wedge \varphi') ; \text{skipstbl}(V) &\equiv (\varphi \wedge \text{skipstbl}(V)) ; (\varphi' \wedge \text{skipstbl}(V)). \end{aligned} \quad (6)$$

For every *infstbl/fs*-formula φ and a finite set of variables V , we will denote by $V \blacktriangleright \varphi$ the formula obtained by replacing each sub-formula $\text{infstbl}(V')$ by $\text{infstbl}(V \cup V')$, and each sub-formula $\text{fs}(V' \mid V'')$ by $\text{fs}(V' \mid V \cup V'')$. By (5) and (6), we immediately obtain that:

$$V \blacktriangleright \varphi \equiv \varphi \wedge \text{skipstbl}(V)^*. \quad (7)$$

4. From box expressions to logical formulas

To make the presentation more accessible, we will first show how to translate non-synchronised box expressions. After that, we will extend the translation to the streamlined synchronised expressions, and, finally, we will deal with the case of general synchronised expressions.

4.1. Translating non-synchronised expressions

The translation for non-synchronised expressions yields *infstbl/fs*-formulas, and is defined compositionally in the following way:

$$\begin{aligned} \text{itl}(\text{stop}) &= \text{infstbl}(\emptyset) \\ \text{itl}(a) &= \text{fs}(a_\epsilon \mid \emptyset) \\ \text{itl}(E ; F) &= ((;_R \cdot \text{var}(\text{itl}(F))) \blacktriangleright (;_L \cdot \text{itl}(E))) ; ((;_L \cdot \text{var}(\text{itl}(E))) \blacktriangleright (;_R \cdot \text{itl}(F))) \\ \text{itl}(E \square F) &= ((\square_R \cdot \text{var}(\text{itl}(F))) \blacktriangleright (\square_L \cdot \text{itl}(E))) \vee ((\square_L \cdot \text{var}(\text{itl}(E))) \vee (\square_R \cdot \text{itl}(F))) \\ \text{itl}(E \parallel F) &= \parallel_L \cdot \text{itl}(E) \wedge \parallel_R \cdot \text{itl}(F) \\ \text{itl}(\llbracket E \otimes F \otimes G \rrbracket) &= (((\otimes_M \cdot \text{var}(\text{itl}(F))) \cup (\otimes_R \cdot \text{var}(\text{itl}(G)))) \blacktriangleright (\otimes_L \cdot \text{itl}(E))) ; \\ &\quad (((\otimes_L \cdot \text{var}(\text{itl}(E))) \cup (\otimes_R \cdot \text{var}(\text{itl}(G)))) \blacktriangleright (\otimes_M \cdot \text{itl}(F)))^* ; \\ &\quad (((\otimes_L \cdot \text{var}(\text{itl}(E))) \cup (\otimes_M \cdot \text{var}(\text{itl}(F)))) \blacktriangleright (\otimes_R \cdot \text{itl}(G))). \end{aligned}$$

As an example, for the expressions generating the boxes in Figure 3, we obtain:

$$\begin{aligned} \text{itl}(a ; b) &= \text{fs}(a_{;L} \mid b_{;R}) ; \text{fs}(b_{;R} \mid a_{;L}) \\ \text{itl}(c \square (a ; b)) &= (\text{fs}(c_{\square L} \mid a_{\square R ; L}, b_{\square R ; R})) \vee (\text{fs}(a_{\square R ; L} \mid b_{\square R ; R}, c_{\square L}) ; \text{fs}(b_{\square R ; R} \mid a_{\square R ; L}, c_{\square L})). \end{aligned}$$

As far as the complexity of the translation is concerned, $|\text{itl}(E)| \in O(|E|^2)$. This compares very favourably with the size of the original semantical representation of E since $|\text{box}(E)| \in O(2^{|E|})$.

Crucially, the step semantics of a non-synchronised box expression and the corresponding ITL formula coincide.

Theorem 4.1. (non-synchronised expression)

If H is a non-synchronised box expression then $sts(itl(H)) = sts(box(H))$, where $sts = infsts$ or $sts = finsts$.

Proof:

Let $\psi = itl(H)$ and $N = box(H)$. The proof proceeds by induction on the structure of H .

Case 1: $H = stop$. Then $\psi = inf \equiv infstbl(\emptyset)$ and $N = N_{stop}$. Hence we have:

$$infsts(\psi) = \emptyset^\omega = infsts(N) \quad \text{and} \quad finsts(\psi) = \emptyset = finsts(N).$$

Case 2: $H = a$. Then, $\psi = fs(a_\epsilon | \emptyset) = skipstbl(a_\epsilon)^* ; flip(a_\epsilon) ; skipstbl(a_\epsilon)^*$ and $N = N_a$. Hence we have:

$$\begin{aligned} infsts(\psi) &= \emptyset^\omega \cup \emptyset^* \cdot \{\{a_\epsilon\}\} \cdot \emptyset^\omega = infsts(N) \\ finsts(\psi) &= \emptyset^* \cdot \{\{a_\epsilon\}\} \cdot \emptyset^* = finsts(N). \end{aligned}$$

Case 3: $H = E \sqcap F$. Then, by the equivalence (7), we have:

$$\psi \equiv \Box_L \cdot itl(E) \wedge skipstbl(\Box_R \cdot var(itl(F)))^* \vee \Box_R \cdot itl(F) \wedge skipstbl(\Box_L \cdot var(itl(E)))^*.$$

Hence, by $var(\psi) = \Box_L \cdot var(itl(E)) \cup \Box_R \cdot var(itl(F))$ as well as (applied in this order) Proposition 3.2, Proposition 3.5, the induction hypothesis, and Proposition 2.3, we obtain:

$$\begin{aligned} sts(\psi) &= sts(\Box_L \cdot itl(E)) \cup sts(\Box_R \cdot itl(F)) \\ &= \Box_L \cdot sts(itl(E)) \cup \Box_R \cdot sts(itl(F)) \\ &= \Box_L \cdot sts(box(E)) \cup \Box_R \cdot sts(box(F)) = sts(H). \end{aligned}$$

Case 4: $H = E \parallel F$. Then, by Proposition 3.2, Proposition 3.5, the induction hypothesis, and Proposition 2.3, we have:

$$\begin{aligned} sts(\psi) &= sts(\parallel_L \cdot itl(E)) \parallel sts(\parallel_L \cdot itl(F)) \\ &= \parallel_L \cdot sts(itl(E)) \parallel \parallel_L \cdot sts(itl(F)) \\ &= \parallel_L \cdot sts(box(E)) \parallel \parallel_L \cdot sts(box(F)) = sts(H). \end{aligned}$$

Case 5: $H = E ; F$. Then, by the equivalence (7), we have:

$$\psi = ;_L \cdot itl(E) \wedge skipstbl(;_R \cdot var(itl(F)))^* ; ;_R \cdot itl(F) \wedge skipstbl(;_L \cdot var(itl(E)))^*.$$

Hence we obtain, by $var(\psi) = ;_L \cdot var(itl(E)) \cup ;_R \cdot var(itl(F))$ as well as (applied in this order) Proposition 3.2, Proposition 3.5, the induction hypothesis, and Proposition 2.4:

$$\begin{aligned} finsts(\psi) &= finsts(;_L \cdot itl(E)) \cdot finsts(;_R \cdot itl(F)) \\ &= ;_L \cdot finsts(itl(E)) \cdot ;_R \cdot finsts(itl(F)) \\ &= ;_L \cdot finsts(box(E)) \cdot ;_R \cdot finsts(box(F)) = finsts(H). \end{aligned}$$

The second part of the proof for sequence is similar.

Case 6: $H = \llbracket E \circledast F \circledast G \rrbracket$. Then, by the equivalence (7), we have:

$$\begin{aligned} \psi &= \circledast_L \cdot \text{itl}(E) \wedge \text{skipstbl}(\circledast_M \cdot \text{var}(F) \cup \circledast_R \cdot \text{var}(G))^* ; \\ &\quad (\circledast_M \cdot \text{itl}(F) \wedge \text{skipstbl}(\circledast_L \cdot \text{var}(E) \cup \circledast_R \cdot \text{var}(G))^*)^* ; \\ &\quad \circledast_R \cdot \text{itl}(G) \wedge \text{skipstbl}(\circledast_L \cdot \text{var}(E) \cup \circledast_M \cdot \text{var}(F))^* \end{aligned}$$

Hence we obtain, by $\text{var}(\psi) = \circledast_L \cdot \text{var}(\text{itl}(E)) \cup \circledast_M \cdot \text{var}(\text{itl}(F)) \cup \circledast_R \cdot \text{var}(\text{itl}(G))$ as well as (applied in this order) Proposition 3.3, Proposition 3.5, the induction hypothesis, and Proposition 2.5:

$$\begin{aligned} \text{finsts}(\psi) &= \text{finsts}(\circledast_L \cdot \text{itl}(E)) \cdot \text{finsts}(\circledast_R \cdot \text{itl}(F))^* \cdot \text{finsts}(\circledast_M \cdot \text{itl}(G)) \\ &= \circledast_L \cdot \text{finsts}(\text{itl}(E)) \cdot (\circledast_R \cdot \text{finsts}(\text{itl}(F)))^* \cdot \circledast_M \cdot \text{finsts}(\text{itl}(G)) \\ &= \circledast_L \cdot \text{finsts}(\text{box}(E)) \cdot (\circledast_R \cdot \text{finsts}(\text{box}(F)))^* \cdot \circledast_M \cdot \text{finsts}(\text{box}(G)) = \text{finsts}(H). \end{aligned}$$

The second part of the proof for iteration is similar. \square

4.2. Translating streamlined expressions

The result captured by Theorem 4.1 is strong as it means that the behavioural properties of non-synchronised box expressions related to the sequencing of executed actions can be re-interpreted as properties of the translated formulas, assuming that an execution of a transition is ‘simulated’ by a flipping of the corresponding Boolean variable. Extending such a result to streamlined expressions highlights the way in which the box expression synchronisation mechanism (through merging transitions) and the ITL synchronisation mechanism (through flipping variables in different parts of a formula) can be made to match each other.

Let $F = E \text{ sco } \rho$ be a streamlined box expression. Then, $\text{itl}(F)$ is obtained from $\text{itl}(E)$ by replacing each occurrence of each variable v by the unique variable in $\text{trans}(v)$ ⁸.

It is also important to stress that it is not necessary to derive $\text{box}(F)$ in order to derive the $\text{trans}(v)$ ’s.

Theorem 4.2. (streamlined expression)

Let $F = E \text{ sco } \rho$ be a streamlined box expression. Then, $\text{sts}(\text{itl}(F)) = \text{sts}(\text{box}(F))$, for $\text{sts} = \text{infsts}$ or $\text{sts} = \text{finsts}$.

Proof:

By the definition of the itl mapping, flipping the value of any variable v in $\text{itl}(E)$ is due to the (unique within $\text{itl}(E)$) sub-formula $\text{flip}(v)$ as otherwise v keeps the same value due to the presence of the $\text{skipstbl}(v)^*$ sub-formulas.

Suppose now that a_W is a variable in $\text{itl}(F)$ and that $a_{\pi_1}^1, \dots, a_{\pi_k}^k$ are the variables in $\text{itl}(E)$ which in $\text{itl}(F)$ are replaced by a_W . Then, by definition, $\pi_i | \pi_j$ for all $i \neq j$. Hence, there is a sub-formula $\varphi \wedge \varphi'$ of $\text{itl}(E)$ such that $\text{var}(\varphi) \cap \text{var}(\varphi') = \emptyset$ and, without loss of generality, $a_{\pi_i}^i \in \text{var}(\varphi)$ and $a_{\pi_j}^j \in \text{var}(\varphi')$. As this observation holds for all distinct i and j , it follows that flipping of a_W in $\text{itl}(F)$ must be ‘agreed upon’ by all the sub-formulas $\text{flip}(a_W)$, each resulting from a replacement of some $a_{\pi_j}^j$ by a_W . The result then follows from $\text{sts}(\text{itl}(E)) = \text{sts}(\text{box}(E))$ (see Theorem 4.1) and Proposition 2.6. \square

⁸Recall $\text{trans}(t)$ was introduced in Section 2.6.

In terms of complexity, the size of $\text{itl}(F)$ is of the same order as that $\text{itl}(E)$, and so $|\text{itl}(F)| \in O(|F|^2)$ which, again, compares very favourably with $|\text{box}(F)|$ as $|\text{box}(F)| \in O(2^{|E|})$. Moreover, the number variables in $\text{itl}(F)$ is the same as the number of transitions in $\text{box}(F)$.

4.3. Translating general expressions

Suppose now that $F = E \text{ sco } \rho$ is an arbitrary synchronised expression. Given Proposition 2.7, we could now simply take the streamlined expression $\text{stl}(F)$ defined in Section 2.6 and, after consistently renaming variables according to the bijection λ defined in Section 2.6, derive $\text{itl}(\text{stl}(F))$ and obtain a generalised version of Theorem 4.2.

Applying our previous complexity estimates, we would get $|\text{itl}(\text{stl}(F))| \in O((|E| + |\text{box}(E)|^F)^2)$. Therefore, $\text{itl}(\text{stl}(F))$ would be of similar size as $\text{box}(E)$. This, however, changes radically if we assumed that, e.g., only binary synchronisations are allowed and a given pair of synchronised actions always yields the same action label,⁹ and so $|\text{box}(E)|^F \in O(|E|^2)$. Then we would have $|\text{itl}(\text{stl}(F))| \in O(|E|^4)$ whereas $|\text{box}(E)|$ would belong to $O(2^{|E|})$.¹⁰ Moreover, as we already argued, it is not necessary to construct $\text{box}(F)$ in order to derive $\text{itl}(\text{stl}(F))$.

As an alternative, we may proceed without pre-processing and conservatively extend the translation defined for non-synchronised expressions. More precisely, for any synchronised expression $F = E \text{ sco } \rho$, we construct $\text{itl}(F)$ directly from $\text{itl}(E)$ by replacing each $\text{flip}(t)$ by $\text{flip}(\text{trans}(t))$, and each $\text{skipstbl}(V)$ by $\text{skipstbl}(\bigcup \text{trans}(V))$. The size of $\text{itl}(F)$ would be similar as that of $\text{itl}(\text{stl}(F))$.

Theorem 4.3. (synchronised expression)

Let $F = E \text{ sco } \rho$ be a synchronised box expression. Then, $\text{sts}(\text{itl}(F)) = \text{sts}(\text{box}(F))$, where $\text{sts} = \text{infsts}$ or $\text{sts} = \text{finsts}$.

Proof:

Let us consider the streamlined expression $\text{stl}(E \text{ sco } \rho) = E' \text{ sco } \rho'$, as defined in Section 2.6, and $\varphi = \text{itl}(E' \text{ sco } \rho')$ as defined for streamlined expression. Then $\text{itl}(E \text{ sco } \rho)$ and φ are equivalent after applying a consistent renaming of variables given by the bijection λ defined in Section 2.6. Now, we observe that if $\text{trans}(a_\pi) = \{b_{W_1}^1, \dots, b_{W_m}^m\}$ and $m \geq 2$, then the sub-formula $\text{fs}(a_\pi \mid \emptyset)$ in $\text{itl}(E)$ is transformed into $\text{fs}(\text{trans}(a_\pi) \mid \emptyset)$ within $\text{itl}(F)$. This is equivalent to

$$\bigvee_{1 \leq i \leq m} \text{fs}(b_{W_i}^i \mid b_{W_1}^1, \dots, b_{W_{i-1}}^{i-1}, b_{W_{i+1}}^{i+1}, \dots, b_{W_m}^m),$$

which in turn can be shown to be equivalent (after taking into account the correspondence given by λ) to $\text{itl}(b_{W_1}^1 \sqcap \dots \sqcap b_{W_m}^m)$. Hence, the result follows from Theorem 4.2 and Propositions 2.7 and 3.4. \square

To conclude, we have demonstrated that it is possible to associate in a computationally efficient way a semantically equivalent ITL formula with any box expression considered in this paper.

⁹Such an assumption is usually made, e.g., by process algebras.

¹⁰Take, for every $n \geq 1$, $F_n = \underbrace{(\text{stop} \parallel \text{stop}) \sqcap \dots \sqcap (\text{stop} \parallel \text{stop})}_{n \text{ times}} \text{ sco } \emptyset$. Then $|F_n| = 2 \cdot n$ and $|\text{box}(F_n)| = 2^{n+1}$.

5. Examples

We will now present examples illustrating the translation of box expressions into logical formulas.

5.1. Synchronisation and restriction

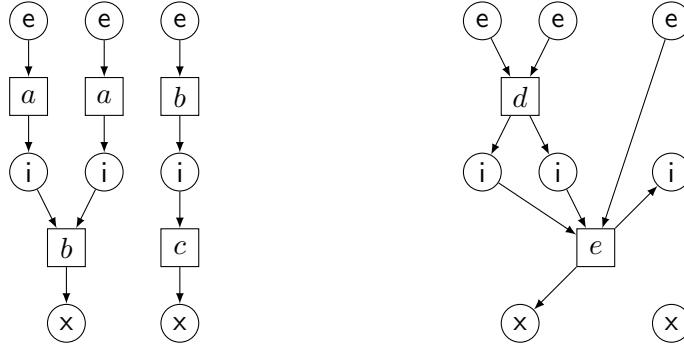


Figure 5. Boxes of $E = ((a \parallel a); b) \parallel (b; c)$ and $F = E \text{ sco } \{aa \mapsto d, bb \mapsto e\}$.

To illustrate action restriction as well as internal and external synchronisations, we consider a streamlined expression $F = E \text{ sco } \rho$, where

$$E = ((a \parallel a); b) \parallel (b; c) \quad \text{and} \quad \rho = \{aa \mapsto d, bb \mapsto e\}.$$

The corresponding boxes are shown in Figure 5. In the translation, we first derive:

$$\begin{aligned} \text{itl}(E) = & ((\text{fs}(a_{\parallel L}; L^{\parallel L} \mid b_{\parallel L}; R) \wedge \text{fs}(a_{\parallel L}; L^{\parallel R} \mid b_{\parallel L}; R)) ; \text{fs}(b_{\parallel L}; R \mid a_{\parallel L}; L^{\parallel L}, a_{\parallel L}; L^{\parallel R})) \\ & \wedge (\text{fs}(b_{\parallel R}; L \mid c_{\parallel R}; R) ; \text{fs}(c_{\parallel R}; R \mid b_{\parallel R}; L)). \end{aligned}$$

To prepare for applying scoping, we derive

$$\begin{aligned} \text{trans}(a_{\parallel L}; L^{\parallel L}) &= \text{trans}(a_{\parallel L}; L^{\parallel R}) = \{d_{\parallel L}; L^{\parallel L}, \parallel L; L^{\parallel R}\} = \{\alpha\} \\ \text{trans}(b_{\parallel L}; R) &= \text{trans}(b_{\parallel R}; L) = \{e_{\parallel L}; R, \parallel R; L\} = \{\beta\} \\ \text{trans}(c_{\parallel R}; R) &= \emptyset \end{aligned}$$

which after simplifications leads to

$$\begin{aligned} \text{itl}(F) &= ((\text{fs}(\alpha \mid \beta) \wedge \text{fs}(\alpha \mid \beta)) ; \text{fs}(\beta \mid \alpha)) \wedge (\text{fs}(\beta \mid \emptyset) ; \text{infstbl}(\beta)) \\ &= (\text{fs}(\alpha \mid \beta) ; \text{fs}(\beta \mid \alpha)) \wedge (\text{fs}(\beta \mid \emptyset) ; \text{infstbl}(\beta)) \\ &= \text{fs}(\alpha \mid \beta) ; \text{fs}(\beta \mid \alpha) ; \text{infstbl}(\alpha, \beta). \end{aligned}$$

Thus $\text{itl}(F)$ is satisfied over an interval provided that the latter can be split into three successive sub-intervals (the first two being finite and the third one infinite) overlapping on single states so that the

variables α and β are kept unchanged, except for one flipping of α within the first interval, and one flipping of β in the second interval (see (4) for the definitions of $\text{fs}(\alpha | \beta)$, $\text{fs}(\beta | \alpha)$, and $\text{infstbl}(\alpha, \beta)$). Note that $\text{finsts}(F) = \emptyset$ and $\text{infsts}(F) = \emptyset^\omega \cup \emptyset^* \cdot \{\{\alpha\}\} \cdot \emptyset^\omega \cup \emptyset^* \cdot \{\{\alpha\}\} \cdot \emptyset^* \cdot \{\{\beta\}\} \cdot \emptyset^\omega$.

5.2. Parallel composition and choice

To illustrate choice and parallel composition, we consider a streamlined expression $F = E \text{ sco } \rho$, where

$$E = (a \sqcap b) \parallel b \quad \text{and} \quad \rho = \{a \mapsto a, bb \mapsto b\}.$$

The corresponding boxes are shown in Figure 6. In the translation, we first derive

$$\text{itl}(E) = (\text{fs}(a_{\parallel L} \sqcap_L | b_{\parallel L} \sqcap_R) \vee \text{fs}(b_{\parallel L} \sqcap_R | a_{\parallel L} \sqcap_L)) \wedge \text{fs}(b_{\parallel R} | \emptyset).$$

To prepare for applying scoping, we derive:

$$\begin{aligned} \text{trans}(a_{\parallel L} \sqcap_L) &= \{a_{\parallel L} \sqcap_L\} = \{\alpha\} \\ \text{trans}(b_{\parallel L} \sqcap_R) &= \text{trans}(b_{\parallel R}) = \{b_{\parallel L} \sqcap_{R, \parallel R}\} = \{\beta\} \end{aligned}$$

which (after eliminating true in conjunctions) leads to

$$\text{itl}(F) = (\text{fs}(\alpha | \beta) \vee \text{fs}(\beta | \alpha)) \wedge \text{fs}(\beta | \emptyset) \equiv (\text{fs}(\alpha | \beta) ; \text{infstbl}(\alpha, \beta)) \vee \text{fs}(\beta | \alpha).$$

Hence $\text{finsts}(F) = \emptyset^* \cdot \{\{\beta\}\} \cdot \emptyset^*$ and $\text{infsts}(F) = \emptyset^\omega \cup \emptyset^* \cdot \{\{\alpha\}, \{\beta\}\} \cdot \emptyset^\omega$.

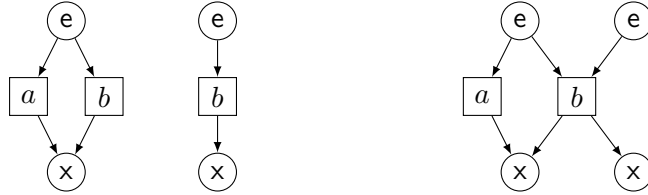


Figure 6. Boxes of $E = (a \sqcap b) \parallel b$ and $F = E \text{ sco } \{a \mapsto a, bb \mapsto b\}$.

5.3. Iteration and scoping

To illustrate synchronisation inside iteration, we consider a non-streamlined expression $F = E \text{ sco } \rho$, where

$$E = \llbracket a \otimes (b \parallel c) \otimes d \rrbracket \quad \text{and} \quad \rho = \{a \mapsto a, bc \mapsto a, c \mapsto e\}.$$

The boxes corresponding to E and F are shown in Figure 4. We first derive

$$\text{itl}(E) = \text{fs}(a_{\otimes L} | b_{\otimes M \parallel L}, c_{\otimes M \parallel R}, d_{\otimes R}) ; \left(\begin{array}{c} \text{fs}(b_{\otimes M \parallel L} | a_{\otimes L}, d_{\otimes R}) \\ \wedge \\ \text{fs}(c_{\otimes M \parallel R} | a_{\otimes L}, d_{\otimes R}) \end{array} \right)^* ; \text{fs}(d_{\otimes R} | a_{\otimes L}, b_{\otimes M \parallel L}, c_{\otimes M \parallel R}).$$

To prepare for applying scoping, we derive

$$\begin{aligned}
\text{trans}(a \circledast_L) &= \{a \circledast_L\} &= \{\gamma\} \\
\text{trans}(d \circledast_R) &= \emptyset \\
\text{trans}(b \circledast_{M \parallel L}) &= \{a \circledast_{M \parallel L}, \circledast_{M \parallel R}\} &= \{\alpha\} \\
\text{trans}(c \circledast_{M \parallel R}) &= \{a \circledast_{M \parallel L}, \circledast_{M \parallel R}, e \circledast_{M \parallel R}\} &= \{\alpha, \zeta\}
\end{aligned}$$

which leads to

$$\begin{aligned}
\text{itl}(F) &= \text{fs}(\gamma \mid \alpha, \zeta) ; (\text{fs}(\alpha \mid \gamma) \wedge \text{fs}(\alpha, \zeta \mid \gamma))^* ; \text{infstbl}(\gamma, \alpha, \zeta) \\
&\equiv \text{fs}(\gamma \mid \alpha, \zeta) ; ((\text{fs}(\alpha \mid \gamma) \wedge \text{fs}(\alpha \mid \gamma, \zeta)) \vee (\text{fs}(\alpha \mid \gamma) \wedge \text{fs}(\zeta \mid \gamma, \alpha)))^* ; \text{infstbl}(\gamma, \alpha, \zeta) \\
&\equiv \text{fs}(\gamma \mid \alpha, \zeta) ; (\text{fs}(\alpha \mid \gamma, \zeta) \vee (\text{fs}(\zeta \mid \gamma, \alpha) ; \text{infstbl}(\gamma, \alpha, \zeta)))^* ; \text{infstbl}(\gamma, \alpha, \zeta).
\end{aligned}$$

Hence $\text{infsts}(F) = \emptyset^\omega \cup \emptyset^* \cdot \{\{\gamma\}\} \cdot \{\emptyset^\omega \cup (\emptyset^* \cdot \{\{\alpha\}\})^* \cdot \emptyset^\omega \cup (\emptyset^* \cdot \{\{\alpha\}\})^* \cdot \emptyset^* \cdot \{\{\zeta\}\} \cdot \emptyset^\omega\}$ and $\text{finsts}(F) = \emptyset$.

5.4. Producer/consumers system

Consider BA expressions G_n ($n \geq 1$), modelling a producer P_n working in parallel with n consumer processes C_1, \dots, C_n . After starting up using action a , the producer repeatedly performs a local action b followed by a parallel execution of communication actions c_1, \dots, c_n , each cycle being finished by the execution of action d , and then a new cycle is started by executing action e . A consumer process C_i , after a start up action a_i , also executes an indefinite loop executing action c_i and ending each cycle with f_i . The definitions of these expressions are as follows:

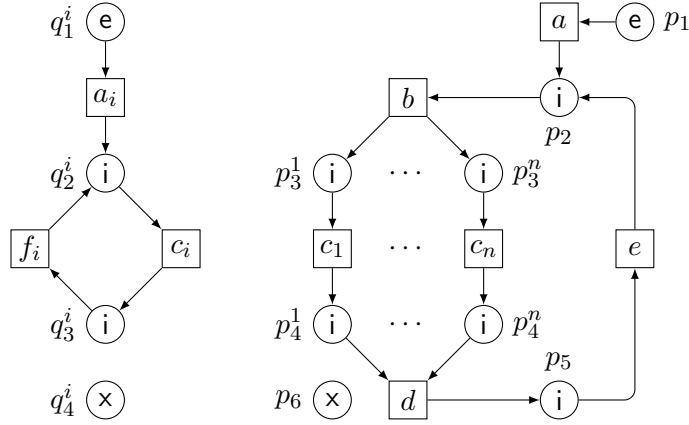
$$\begin{aligned}
C_i &= \llbracket a_i \circledast c_i ; f_i \circledast \text{stop} \rrbracket & (i = 1, \dots, n) \\
P_n &= \llbracket a \circledast b ; (c_1 \parallel \dots \parallel c_n) ; d ; e \circledast \text{stop} \rrbracket \\
PC_n &= (C_1 \parallel \dots \parallel C_n \parallel P_n) \text{ sco } \rho
\end{aligned}$$

where

$$\rho = \left\{ \begin{array}{llll} c_1 c_1 \mapsto \tau_1 & \dots & c_n c_n \mapsto \tau_n & a \mapsto a \\ a_1 \mapsto a_1 & \dots & a_n \mapsto a_n & d \mapsto d \quad b \mapsto b \\ f_1 \mapsto f_1 & \dots & f_n \mapsto f_n & e \mapsto e \end{array} \right\}$$

Figure 7 depicts the boxes of C_i and P_n . Moreover, Figure 8 shows the box corresponding to PC_2 together with the initial marking. The corresponding ITL formula is shown below (for brevity, we only indicate variables which flip their values, but even then we omit the subscripts, and show only the labels of transitions corresponding to these variables):

$$\begin{aligned}
\text{itl}(PC_n) &= (\text{fs}(a_1 \mid) ; (\text{fs}(\tau_1 \mid) ; \text{fs}(f_1 \mid))^* ; \text{infstbl}()) \wedge \dots \wedge \\
&(\text{fs}(a_n \mid) ; (\text{fs}(\tau_n \mid) ; \text{fs}(f_n \mid))^* ; \text{infstbl}()) \wedge \\
&(\text{fs}(a \mid) ; (\text{fs}(b \mid) ; (\text{fs}(\tau_1 \mid) \wedge \dots \wedge \text{fs}(\tau_n \mid)) ; \text{fs}(d \mid) ; \text{fs}(e \mid))^* ; \text{infstbl}()).
\end{aligned}$$

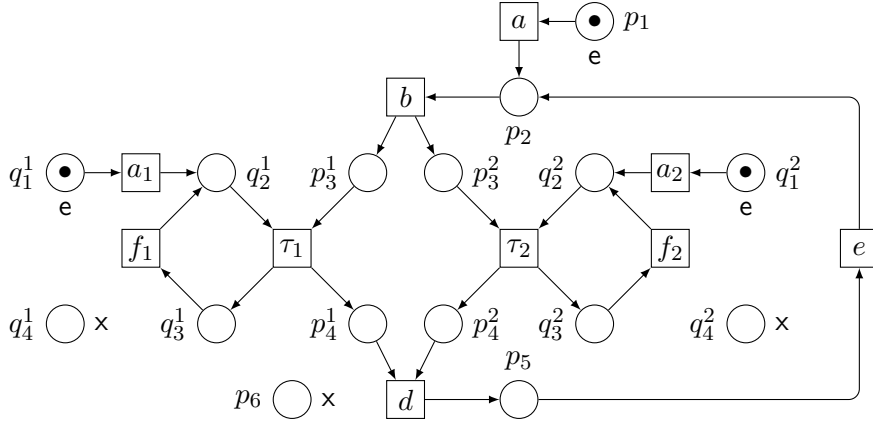
Figure 7. The boxes of the consumers C_i and the producer P_n .

6. Conclusions

In the past, logics have been mainly used for expressing correctness properties of systems specified using Petri nets [36]. When it comes to the intrinsic semantic relationship between logics and Petri nets, we feel that the work on the connections between linear logic [37] and Place Transition nets has been the closest one. However, the main concern there was the handling of multiple token occurrences in net places whereas here nets can hold at most two tokens in a single place. Another way in which logics and Petri nets are related is reported in [38], which provided a characterisation of Petri net languages in terms of second-order logical formulas.

The results presented in this paper demonstrate that one can define a translation from box expressions to ITL with equivalent behaviour. We also show that the complexity of the proposed translation compares favourably with the complexity of the translation from box expressions to boxes.

It is therefore important to further investigate the extent to which the established connection between BA and ITL could be generalised and exploited. In particular, we plan to investigate what is the subset of ITL which can be translated into BA. We do not expect that such a translation will be easy for the full ITL for at least two reasons. The first is that the projection operator prj of ITL does not have an equivalent in BA and so incorporating it would call for new constructs at the syntactical and semantical levels. The second reason is that ITL allows one to specify actions happening simultaneously, e.g., $\text{flip}(v) \wedge \text{flip}(w)$, as in $\text{flip}(v) \wedge \text{flip}(w)$. This is akin to executing the step $\{v, w\}$ without being able to execute $\{v\}\{w\}$ or $\{w\}\{v\}$, i.e., v and w must be executed synchronously and without delay. Such a behaviour cannot be reproduced in the standard Petri nets (and also boxes) as they are inherently asynchronous models. The required effect could perhaps be achieved by using the maximally concurrent execution rule, but this would fundamentally change the net model. In fact, we feel that in logical formulas translatable to box expressions all actions should be fully asynchronous, using $\text{fs}(v \mid \emptyset)$ rather than v in ITL syntax. However, more investigation needs to be conducted in order, in particular, to generate a suitable scoping set from a give ITL formula.

Figure 8. Initially marked box of PC_2 .

A long-term goal is the development of a hybrid verification methodology combining ITL and BA techniques. For example, sequential algorithms and infinite data structures could be treated by ITL techniques [39, 40, 41, 30], while intensive parallel or communicating aspects of systems could be treated by net unfoldings [42, 43] or other Petri net techniques [44].

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